



Semiconductor Packaging for the Age of Digitalization: New Materials, Technologies and Processes

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Message from the Guest Editor

We are continuously observing new challenges in microelectronic packaging. Major trends like the Internet of Things, digitalization, and the application of Artificial Intelligence (AI) drive new requirements for hardware: new chip and system architecture, an increasing number of electric interconnects, low power consumption and miniaturization. In addition, autonomous intelligent systems such as self-driving cars will cause new challenges for functional safety, reliability, and lifetime prediction in the field of electronic devices. Cost-efficient methods for condition monitoring needs to be integrated on package level and realized on system level. Another major trend is the transition of energy supply and electrification, which affects microelectronic packaging. Power electronics, e.g., converters and electromobility, enable, in combination with wide gap semiconductors, high operation temperature under severe environmental conditions which are demanding for electronic materials. The upcoming age of digitalization and Artificial Intelligence will be based on the hardware—based on new material, technologies, and packaging processes.





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Message from the Editor-in-Chief

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