Hardware-Friendly Machine Learning and Its Applications

Guest Editor:

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Message from the Guest Editor

Dear Colleagues,

This Special Issue aims to explore the potential of efficient machine learning, reveal emerging algorithms and design needs, and promote novel applications. It will also collect contributions on the advancement of methodologies and technologies for the design, evaluation, and optimization of software, hardware, and emerging applications representing the current solution to support the diverse computing scenarios in which machine learning is exploited.

Topics of interest include, but are not limited to, the following:

- New microarchitecture designs of hardware accelerators for ML;
- Sparse learning, feature extraction, and personalization;
- Deep learning with high speed and high power efficiency;
- Computing models and hardware architecture co-design for machine learning;
- New microarchitecture designs of hardware accelerators using emerging devices;
- Tools for the modeling, simulation, and synthesis of hardware accelerators
- ML acceleration for edge computing and IoT.

Special Issue

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Message from the Editor-in-Chief

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