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## **Innovation in Nanoelectronic Semiconductor Devices and Materials**

Guest Editors:

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Deadline for manuscript submissions:

closed (1 January 2024)

# **Message from the Guest Editors**

Dear Colleagues,

From cell phone to mainframe computer, precision surgery to autonomous driving, semiconductors have been playing pivotal roles in our daily lives. The emergence of 3D devices and heterogeneous integration brings forth opportunities to fulfill critical specifications.

This Special Issue aims to provide a forum for the most upto-date, high-caliber research efforts in nanoelectronic semiconductor devices and materials. Potential topics include, but are not limited to:

Novel FINFET and nanosheet device architectures and characterization.

Materials innovations in gate-all-around nanosheet devices.

SiGe and new strained layers for devices.

Gate materials and processing for nano devices.

Materials and processing issues for 3D NAND memories.

Electrode materials and characterization for PCM, MRAM, and other memory devices.

ALD and CVD materials for semiconductors.

Advanced BEOL interconnects and metallizations.

FCVD, SOG, and polymeric materials in advanced semiconductor devices

See more information in: https://www.mdpi.com/si/150585

Dr. Wei-Tsu Tseng Dr. Victor Chan Guest Editors











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## **Editor-in-Chief**

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# **Message from the Editor-in-Chief**

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